	Type	Hits	Search Text	DBs	Time Stamp	Comments
1	IS&R	2	("5467277").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:31	
2	BRS	29390	test$3 with (PCB or (printed adj circuit adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:32	
3	BRS	7	test$3 with (PCB or (printed adj circuit adj board)) and substrate$1 with optical with transceiver$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:34	
4	BRS	6198	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connection$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:34	
5	BRS	1678	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:35	
6	BRS	353	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:35	

Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	249	test$3 with (PCB or (printed adi circuit adj board)) and electric$4 with connection$1 and connect$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:35	
BRS	0	test$3 with (PCB or (printed adi circuit adj board)) and electric$4 with connection$1 and connect$3 adj base and surface with (subcomponent$1 or (sub-component$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:36	
BRS	8	test$3 with (PCB or (printed adi circuit adj board)) and electric$4 with connection$1 and connect$3 adj base and (subcomponent$1 or (sub-component$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:37	

Type	Hits	Search Text	DBs	Time Stamp	Comments
10 BRS	4	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connection$1 and connect$3 adj base and (subcomponent$1 or (sub-component$1)) and secur$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:38	
11 BRS	4	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 adj base and (subcomponent$1 or (sub-component$1)) and secur$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:39	
12 BRS	50	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base and (subcomponent$1 or (sub-component$1)) and secur$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:39	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
13	BRS	15	test$3 with (PCB or (printed adi circuit adj board)) and electric$4 with connect$4 and connect$3 with base and (subcomponent$1 or (sub-component$1)) and secur$3 and (transceiver$1 or interface or coupon) with board$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:42	
14	BRS	0	test$3 with (PCB or (printed adi circuit adj board)) and electric$4 with connect$4 and connect$3 adj base and (subcomponent$1 or (sub-component$1)) and secur$3 or (transceiver$1 or interface or coupon) with board$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:42	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
15	BRS	4	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 adj base and secur$3 and (transceiver$1 or interface or coupon) with board$1 with (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:43	
16	BRS	22	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 adj base and secur$3 and (transceiver$1 or interface or coupon) with board$1 and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:43	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
17	BRS	20	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or interface or coupon) with board$1 and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:58	
18	BRS	2	"2004091231"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:57	
19	BRS	57	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or transmit$4 or receiv$4 or interface or coupon) with board$1 and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 09:59	

Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	26	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or transmit$4 or receiv$4 or interface or coupon) with board$1 and (subcomponent$1 or (sub-component$1) or component$1) and upper with surface$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:01	
BRS	16	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or transmit$4 or receiv$4 or interface or coupon) with board$1 and (subcomponent$1 or (sub-component$1) or component$1) and trace$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:07	
20					
21					

Type	Hits	Search Text	DBs	Time Stamp	Comments
22 BRS	13	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or transmit$4 or receiv$4 or interface or coupon) with board$1 and (subcomponent$1 or (sub-component$1) or component$1) and trace$1 and expose$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:04	
23 BRS	5	"4384755".uref.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:06	

Type	Hits	Search Text	DBs	Time Stamp	Comments
24 BRS	1	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and trace$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:08	
25 BRS	11	test$3 with (PCB or (printed adj circuit adj board)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:08	

Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	11	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base with secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:17	
BRS	11	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 with secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 10:18	

Type	Hits	Search Text	DBs	Time Stamp	Comments
28	BRS	25	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$3 and connect$3 with base$1 same secur$3 and (transmit$4 with or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 11:03	
29	BRS	745	702/117.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/22 11:32	
30	BRS	521	209/571,573.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/22 11:33	
31	BRS	1234	324/539,755.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/22 11:35	
32	BRS	206	710/64.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/22 11:34	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
33	BRS	9399	324/539,754,755,757,758,76 5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:39	
34	BRS	6	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and (remov$4 or insert$4 or attach$3) with socket$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:45	

Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	10	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and socket$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:44	
BRS	10	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and (remov$4 or insert$4 or attach$3) and socket$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:45	

Row labels at left: 35, 36

	Type	Hits	Search Text	DBs	Time Stamp	Comments
37	BRS	10	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and socket$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:46	
38	BRS	10	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and socket	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:48	

Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	17	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and pin$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 11:58	
BRS	3	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and base with (insert$4 or attach$4 or receiv$3) with component$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 12:02	

39
40

Type	Hits	Search Text	DBs	Time Stamp	Comments
41 BRS	17	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and base with (insert$4 or attach$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 12:03	
42 BRS	12	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and base with (insert$4 or attach$4) and pin$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 12:03	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
43	BRS	151	439/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 12:08	
44	BRS	8	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and electric$4 with (communication$1 or interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 14:07	

Type	Hits	Search Text	DBs	Time Stamp	Comments
45	BRS	4	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and electric$4 with interface$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 12:15	
46	BRS	21	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and computer$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/22 12:29	

Type	Hits	Search Text	DBs	Time Stamp	Comments
47	BRS	21	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and connect$3 with base$1 same secur$3 and (transceiver$1 or (transmit$4 with receiv$4)) and (subcomponent$1 or (sub-component$1) or component$1) and computer$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 11:19	
48	IS&R	2	("6524123").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 11:19	
49	BRS	233	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and transceiver$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:29	
50	BRS	44	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and transceiver$1 and oppos$3 with end$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 14:09	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
51	BRS	32	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 and transceiver$1 and oppos$3 with end$1 and computer$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 14:39	
52	BRS	31	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 and transceiver$1 and conduct$5 and oppos$3 with end$1 and computer$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 14:10	
53	BRS	2	test$3 with (PCB or (printed adj circuit$1 adj board$1)) and electric$4 with connect$4 and transceiver$1 and conduct$5 and oppos$3 with end$1 and computer$4 and clamp$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 14:11	
54	BRS	4	test$3 with (PCB or (printed adj circuit$1 adj board$1) and electric$4 with connect$4 and transceiver$1 and clamp$1 and receptacle$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:13	

Type	Hits	Search Text	DBs	Time Stamp	Comments
55	BRS	0	computer$4 same test$3 with (PCB or (printed adi circuit$1 adj board$1)) and transceiver$1 and clamp$1 and receptacle$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:09	
56	BRS	3	computer$4 and test$3 with (PCB or (printed adi circuit$1 adj board$1)) and transceiver$1 and clamp$1 and receptacle$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:09	
57	BRS	146	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and clamp$1 and receptacle$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:14	
58	BRS	6	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and clamp$1 and receptacle$1 with oppos$3 with end$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:56	

Type	Hits	Search Text	DBs	Time Stamp	Comments
59 BRS	6	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and computer$4 and receptacle$1 with oppos$3 with end$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:17	
60 BRS	262	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and computer$4 and secur$3 and clamp$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:19	
61 BRS	97	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and computer$4 and secur$3 with clamp$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:19	

Type	Hits	Search Text	DBs	Time Stamp	Comments
62 BRS	82	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and computer$4 and secur$3 with clamp$1 and (transceiver$1 or transmit$4 or receiv$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:20	
63 BRS	52	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and clamp$1 with head$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:30	
64 BRS	0	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and clamp$1 with head$2 and computer$4 with port$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:30	
65 BRS	7	test$3 with (PCB or (printed adi circuit$1 adj board$1)) and electric$4 with connect$4 and clamp$1 with head$2 and computer$4 and port$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 15:35	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
66	IS&R	0	("6887109").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/25 15:36	
67	BRS	2	"20040087216"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/25 15:38	
68	BRS	2	"20040091231"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/25 15:38	
69	BRS	591	test$3 with (PCB or (printed adi circuit$1 adj board$1)) with electric$4 with connect$4 and computer$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/25 16:04	
70	BRS	23	test$3 with (PCB or (printed adi circuit$1 adj board$1)) with electric$4 with connect$4 and computer$4 with port$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/25 16:05	

Type	Hits	Search Text	DBs	Time Stamp	Comments
71 BRS	12	test$3 with (PCB or (printed adj circuit$1 adj board$1)) with electric$4 with connect$4 and computer$4 with port$1 and secur$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 16:32	
72 BRS	0	test$3 with (PCB or (printed adj circuit$1 adj board$1)) with electric$4 with connect$4 and clamp$1 with head$3 with arm$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 16:33	
73 BRS	33	test$3 with (PCB or (printed adj circuit$1 adj board$1)) with electric$4 with connect$4 and clamp$1 with arm$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 16:33	
74 BRS	1	test$3 with (PCB or (printed adj circuit$1 adj board$1)) with electric$4 with connect$4 and clamp$1 with head$3 same arm$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/25 16:33	